

# LM158/LM258/LM358/LM2904 Low Power Dual Operational Amplifiers

**Advantages** 

Eliminates need for dual supplies

Compatible with all forms of logic

Power drain suitable for battery operation

package

to GND

amplifier

Features

Large dc voltage gain

or dual supplies

Low input offset voltage

Wide bandwidth (unity gain)

(temperature compensated)

dependent of supply voltage

Wide power supply range: Single supply

### **General Description**

The LM158 series consists of two independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

Application areas include transducer amplifiers, dc gain blocks and all the conventional op amp circuits which now can be more easily implemented in single power supply systems. For example, the LM158 series can be directly operated off of the standard +5V power supply voltage which is used in digital systems and will easily provide the required interface electronics without requiring the additional  $\pm 15V$ power supplies.

## **Unique Characteristics**

- In the linear mode the input common-mode voltage range includes ground and the output voltage can also swing to ground, even though operated from only a single power supply voltage.
- The unity gain cross frequency is temperature compensated.
- The input bias current is also temperature compensated.

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ow Power Dual Operational Amplifiers. .M158/LM258/LM358/LM2904 Two internally compensated op amps in a single ■ Allows directly sensing near GND and V<sub>OUT</sub> also goes ■ Pin-out same as LM1558/LM1458 dual operational ■ Internally frequency compensated for unity gain 100 dB 1 MHz 3V to 32V  $\pm$  1.5V to  $\pm$  16V Very low supply current drain (500 µA)-essentially in-2 mV Input common-mode voltage range includes ground Differential input voltage range equal to the power sup-

ply voltage 0V to  $V^+ - 1.5V$ Large output voltage swing **Connection Diagrams** (Top Views) DIP/SO Package Metal Can Package OUTPUT A OUTPUT OUTPUT B INVERTING INPUT A ОИТРИТ В INVERTING NVERTING INPUT A INPUT B NON-INVERTING INVERTING INPUT B INPUT A NON-INVERTING NON-INVERTING INPUT A INPUT B NON-INVERTING GND TL/H/7787-1 TL/H/7787-2 Order Number LM158AH, LM158AH/883\* Order Number LM158J, LM158J/883\*, LM158H, LM158H/883\*, LM258H or LM358H LM158AJ or LM158AJ/883\* See NS Package Number H08C See NS Package Number J08A Order Number LM358M, LM358AM or LM2904M See NS Package Number M08A Order Number LM358AN, LM358N or LM2904N See NS Package Number N08E \*LM158 is available per SMD #5962-8771001 LM158A is available per SMD #5962-8771002

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### **Absolute Maximum Ratings**

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 9)

LI	LM158/LM258/LM358 M158A/LM258A/LM35	LM2904	ı	LM158/LM258/LM358 M158A/LM258A/LM358A	LM2904
Supply Voltage, V <sup>+</sup> Differential Input Voltage Input Voltage	32V 32V -0.3V to +32V	26V 26V -0.3V to +26V	Operating Temperature Range LM358 LM258 LM158	0°C to +70°C -25°C to +85°C -55°C to +125°C	-40°C to +85°C
Power Dissipation (Note 1) Molded DIP Metal Can Small Outline Package (M)	830 mW 550 mW 530 mW	830 mW 530 mW	Storage Temperature Range Lead Temperature, DIP (Soldering, 10 seconds)	-65°C to +150°C 260°C	-65°C to +150°C 260°C
Output Short-Circuit to GND (One Amplifier) (Note 2) $V^+ \le 15V$ and $T_A = 25^{\circ}C$	Continuous	Continuous	Lead Temperature, Metal Can (Soldering, 10 seconds) Soldering Information	300°C	300°C
Input Current (V <sub>IN</sub> < -0.3V) (Note 3)	50 mA	50 mA	Dual-In-Line Package Soldering (10 seconds) Small Outline Package Vapor Phase (60 seconds) Infrared (15 seconds)	260°C 215°C 220°C	260°C 215°C 220°C

See AN-450 "Surface Mounting Methods and Their Effect on Product<br/>Reliability" for other methods of soldering surface mount devices.ESD Tolerance (Note 10)250V250V

# **Electrical Characteristics** $V^+ = +5.0V$ , unless otherwise stated

Parameter	Conditions	LM158A			LM358A			LM158/LM258			LM358			LM2904			Units
Falameter	Conditions	Min .	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	onito
Input Offset Voltage	(Note 5), T <sub>A</sub> = 25°C		1	2		2	3		2	5		2	7		2	7	mV
Input Bias Current	$I_{IN(+)}$ or $I_{IN(-)}$ , $T_A = 25^{\circ}$ C, $V_{CM} = 0$ V, (Note 6)		20	50		45	100		45	150		45	250		45	250	nA
Input Offset Current	$I_{IN(+)} - I_{IN(-)}, V_{CM} = 0V, T_A = 25^{\circ}C$		2	10		5	30		3	30		5	50		5	50	nA
Input Common-Mode Voltage Range	V <sup>+</sup> = 30V, (Note 7) (LM2904, V <sup>+</sup> = 26V), T <sub>A</sub> = 25°C	0		V <sup>+</sup> -1.5	0		V <sup>+</sup> -1.5	0		V <sup>+</sup> -1.5	0		V <sup>+</sup> -1.5	0		V <sup>+</sup> -1.5	v
Supply Current	Over Full Temperature Range $R_L = \infty$ on All Op Amps $V^+ = 30V$ (LM2904 $V^+ = 26V$ ) $V^+ = 5V$		1 0.5	2 1.2		1 0.5	2 1.2		1 0.5	2 1.2		1 0.5	2 1.2		1 0.5	2 1.2	mA mA

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Parameter Conditions		Conditions	LM158A			LM358A			LM	158/LM	258		LM358			LM2904		Unit
		Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max		
Large Signal Voltage Gain		$ \begin{array}{l} V^+ = 15V, T_A = 25^\circC, \\ R_L \geq 2k\Omega,  (\text{For }V_O = 1V \\ \text{to 11V}) \end{array} $	50	100		25	100		50	100		25	100		25	100		V/n
$ \begin{array}{ll} \mbox{Common-Mode} & T_{\rm A} = 25^{\circ}\mbox{C}, \\ \mbox{Rejection Ratio} & V_{\rm CM} = 0\mbox{V to V}^+ - 1.5\mbox{V} \end{array} $			70	85		65	85		70	85		65	85		50	70		dE
Power Supply Rejection Ratio		$V^+ = 5V \text{ to } 30V$ (LM2904, $V^+ = 5V$ to 26V), $T_A = 25^{\circ}C$	65	100		65	100		65	100		65	100		50	100		dl
Amplifier-to-Amplifier Coupling		$f = 1 \text{ kHz to } 20 \text{ kHz}, T_A = 25^{\circ}\text{C}$ (Input Referred), (Note 8)		-120			-120			-120			-120			-120		d
Output Current	Source	$ \begin{array}{l} V_{IN}^{+} = 1V, \\ V_{IN}^{-} = 0V, \\ V^{+} = 15V, \\ V_{O}^{-} = 2V, T_{A}^{-} = 25^{\circ}C \end{array} $	20	40		20	40		20	40		20	40		20	40		m
	Sink		10	20		10	20		10	20		10	20		10	20		m
		$ \begin{array}{l} V_{IN}{}^- = 1V, \\ V_{IN}{}^+ = 0V \\ T_A = 25^\circ C, V_O = 200 \text{ mV}, \\ V^+ = 15V \end{array} $	12	50		12	50		12	50		12	50		12	50		μ
Short Circuit to Ground		$T_A = 25^{\circ}$ C, (Note 2), V <sup>+</sup> = 15V		40	60		40	60		40	60		40	60		40	60	m
Input Offset Volt	age	(Note 5)			4			5			7			9			10	m
Input Offset Voltage Drift		$R_{S} = 0\Omega$		7	15		7	20		7			7			7		μV
Input Offset Current		$I_{IN(+)} - I_{IN(-)}$			30			75			100			150		45	200	n,
Input Offset Curr Drift	rent	$R_{S} = 0\Omega$		10	200		10	300		10			10			10		pA/
Input Bias Curre	nt	$I_{IN(+)}$ or $I_{IN(-)}$		40	100		40	200		40	300		40	500		40	500	n,

Parameter		Conditions		LM158A			LM358A			LM158/LM258			LM358			LM2904			Units
					Тур	Мах	Min	Тур	Мах	Min	Тур	Max	Min	Тур	Мах	Min	Тур	Мах	onita
Input Common-I Voltage Range	Mode V <sup>+</sup> = 30 V, (Note 7) (LM2904, V <sup>+</sup> = 26V)			0		V <sup>+</sup> -2	0		V <sup>+</sup> -2	0		V <sup>+</sup> -2	0		V <sup>+</sup> -2	0		V+ -2	V
Large Signal Vo Gain	ltage	$V^+ = +15V$ $(V_0 = 1V \text{ to } 11V)$ $R_L \ge 2 \text{ k}\Omega$		25			15			25			15			15			V/mV
Output Voltage Swing -	V <sub>OH</sub>	$V^{+} = +30V$	$R_L = 2 k\Omega$	26			26			26			26			22			V
		$(LM2904, V^+ = 26V)$	$R_L = 10  k\Omega$	27	28		27	28		27	28		27	28		23	24		v
	V <sub>OL</sub>	$V^+ = 5V, R_L = 10 \text{ k}\Omega$	}		5	20		5	20		5	20		5	20		5	100	mV
Output Current	Source	$V_{IN}^{+} = +1V, V_{IN}^{-} = V^{+} = 15V, V_{O}^{-} = 2V$	0V,	10	20		10	20		10	20		10	20		10	20		mA
	Sink	$V_{IN}^{-} = +1V, V_{IN}^{+} = V^{+} = 15V, V_{O} = 2V$	0V,	10	15		5	8		5	8		5	8		5	8		mA

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Note 1: For operating at high temperatures, the LM358/LM358A, LM2904 must be derated based on a +125°C maximum junction temperature and a thermal resistance of 120°C/W which applies for the device soldered in a printed circuit board, operating in a still air ambient. The LM258/LM258A and LM158/LM158A can be derated based on a + 150°C maximum junction temperature. The dissipation is the total of both amplifiers—use external resistors, where possible, to allow the amplifier to saturate or to reduce the power which is dissipated in the integrated circuit.

Note 2: Short circuits from the output to V+ can cause excessive heating and eventual destruction. When considering short circuits to ground, the maximum output current is approximately 40 mA independent of the magnitude of V<sup>+</sup>. At values of supply voltage in excess of +15V, continuous short-circuits can exceed the power dissipation ratings and cause eventual destruction. Destructive dissipation can result from simultaneous shorts on all amplifiers. Note 3: This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the op amps to go to the V+ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than -0.3V (at 25°C). Note 4: These specifications are limited to -55°C ≤ T<sub>A</sub> ≤ +125°C for the LM158/LM158A. With the LM258/LM258A, all temperature specifications are limited to -25°C ≤ T<sub>A</sub> ≤ +85°C, the LM358/LM358A temperature specifications are limited to 0°C  $\leq$  T<sub>A</sub>  $\leq$  +70°C, and the LM2904 specifications are limited to -40°C  $\leq$  T<sub>A</sub>  $\leq$  +85°C.

Note 5:  $V_0 \simeq 1.4V$ ,  $B_S = 0.0$  with V<sup>+</sup> from 5V to 30V: and over the full input common-mode range (0V to V<sup>+</sup> - 1.5V) at 25°C. For LM2904, V<sup>+</sup> from 5V to 26V.

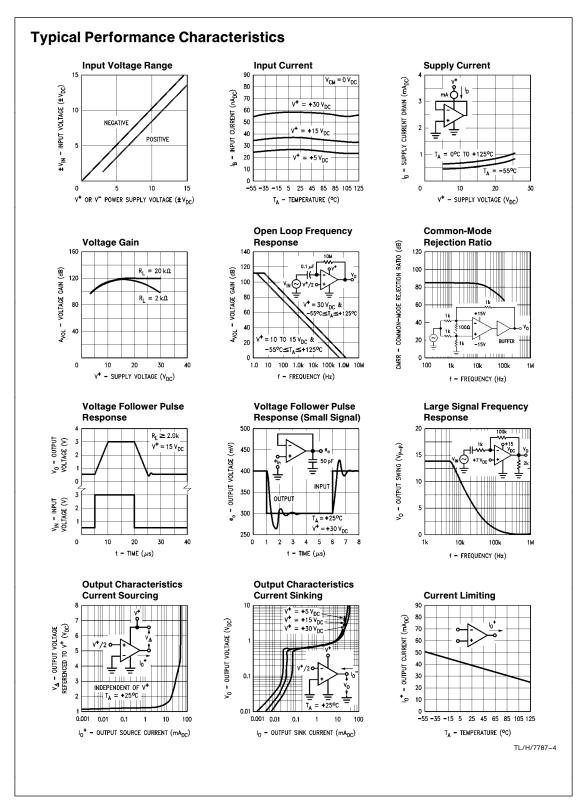
Note 6: The direction of the input current is out of the IC due to the PNP input stage. This current is essentially constant, independent of the state of the output so no loading change exists on the input lines.

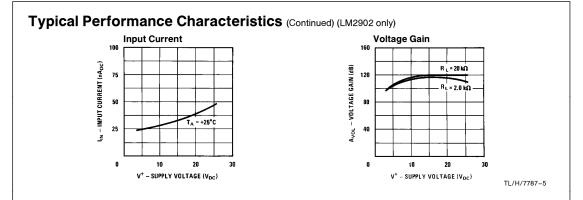
Note 7: The input common-mode voltage of either input signal voltage should not be allowed to go negative by more than 0.3V (at 25°C). The upper end of the common-mode voltage range is V<sup>+</sup> - 1.5V (at 25°C), but either or both inputs can go to +32V without damage (+26V for LM2904), independent of the magnitude of V+.

Note 8: Due to proximity of external components, insure that coupling is not originating via stray capacitance between these external parts. This typically can be detected as this type of capacitance increases at higher frequencies. Note 9: Refer to RETS158AX for LM158A military specifications and to RETS158X for LM158 military specifications.

Note 10: Human body model, 1.5 k $\Omega$  in series with 100 pF.

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### **Application Hints**

The LM158 series are op amps which operate with only a single power supply voltage, have true-differential inputs, and remain in the linear mode with an input common-mode voltage of 0 V<sub>DC</sub>. These amplifiers operate over a wide range of power supply voltage with little change in performance characteristics. At 25°C amplifier operation is possible down to a minimum supply voltage of 2.3 V<sub>DC</sub>.

Precautions should be taken to insure that the power supply for the integrated circuit never becomes reversed in polarity or that the unit is not inadvertently installed backwards in a test socket as an unlimited current surge through the resulting forward diode within the IC could cause fusing of the internal conductors and result in a destroyed unit.

Large differential input voltages can be easily accomodated and, as input differential voltage protection diodes are not needed, no large input currents result from large differential input voltages. The differential input voltage may be larger than V<sup>+</sup> without damaging the device. Protection should be provided to prevent the input voltages from going negative more than  $-0.3~V_{DC}$  (at 25°C). An input clamp diode with a resistor to the IC input terminal can be used.

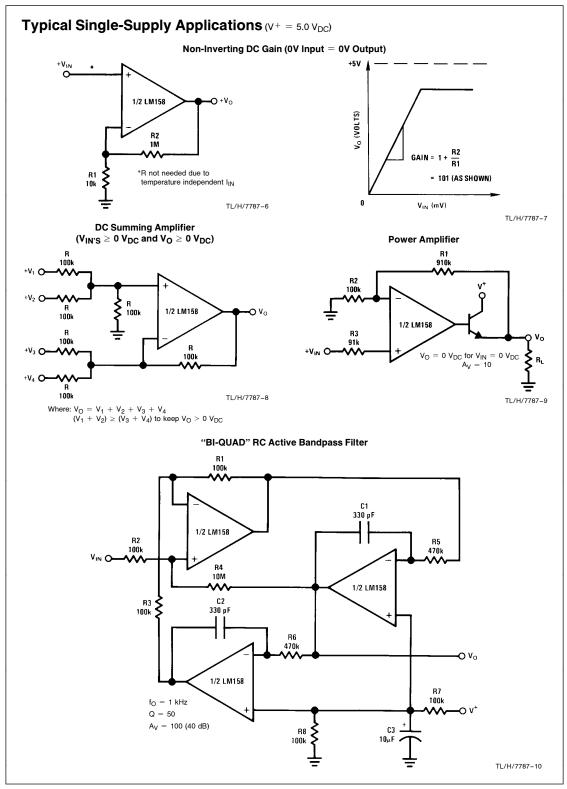
To reduce the power supply current drain, the amplifiers have a class A output stage for small signal levels which converts to class B in a large signal mode. This allows the amplifiers to both source and sink large output currents. Therefore both NPN and PNP external current boost transistors can be used to extend the power capability of the basic amplifiers. The output voltage needs to raise approximately 1 diode drop above ground to bias the on-chip vertical PNP transistor for output current sinking applications.

For ac applications, where the load is capacitively coupled to the output of the amplifier, a resistor should be used, from the output of the amplifier to ground to increase the class A bias current and prevent crossover distortion. Where the load is directly coupled, as in dc applications, there is no crossover distortion. Capacitive loads which are applied directly to the output of the amplifier reduce the loop stability margin. Values of 50 pF can be accomodated using the worst-case non-inverting unity gain connection. Large closed loop gains or resistive isolation should be used if larger load capacitance must be driven by the amplifier.

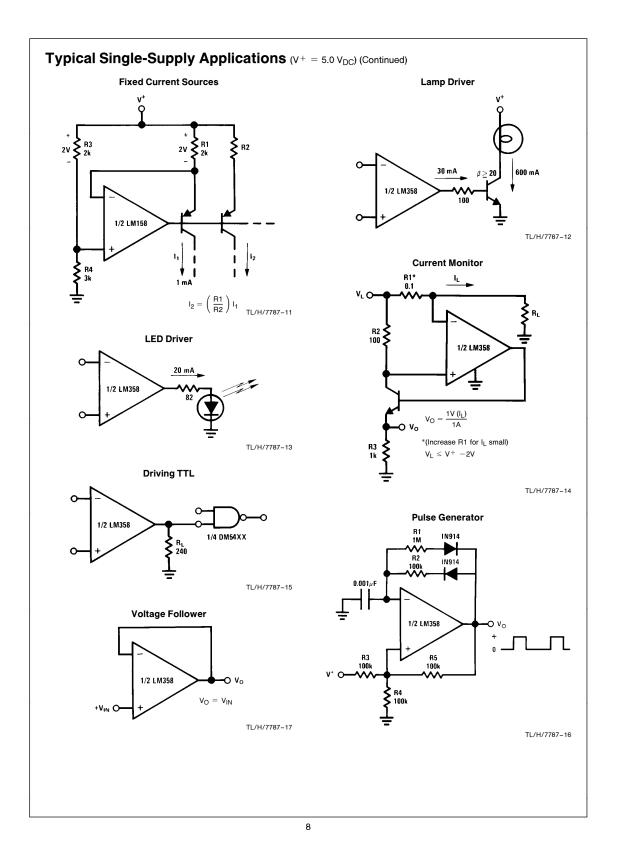
The bias network of the LM158 establishes a drain current which is independent of the magnitude of the power supply voltage over the range of 3  $V_{DC}$  to 30  $V_{DC}$ .

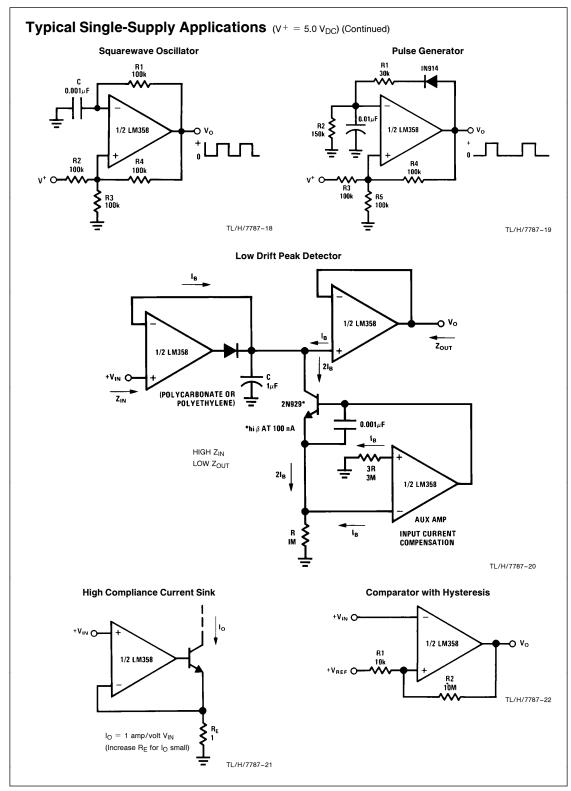
Output short circuits either to ground or to the positive power supply should be of short time duration. Units can be destroyed, not as a result of the short circuit current causing metal fusing, but rather due to the large increase in IC chip dissipation which will cause eventual failure due to excessive function temperatures. Putting direct short-circuits on more than one amplifier at a time will increase the total IC power dissipation to destructive levels, if not properly protected with external dissipation limiting resistors in series with the output leads of the amplifiers. The larger value of output source current which is available at 25°C provides a larger output current capability at elevated temperatures (see typical performance characteristics) than a standard IC op amp.

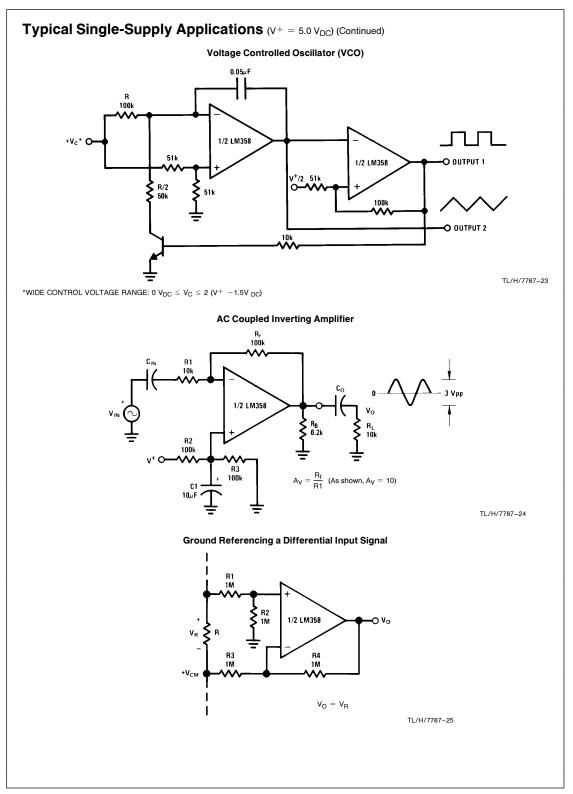
The circuits presented in the section on typical applications emphasize operation on only a single power supply voltage. If complementary power supplies are available, all of the standard op amp circuits can be used. In general, introducing a pseudo-ground (a bias voltage reference of V<sup>+</sup>/2) will allow operation above and below this value in single power supply systems. Many application circuits are shown which take advantage of the wide input common-mode voltage range which includes ground. In most cases, input biasing is not required and input voltages which range to ground can easily be accommodated.

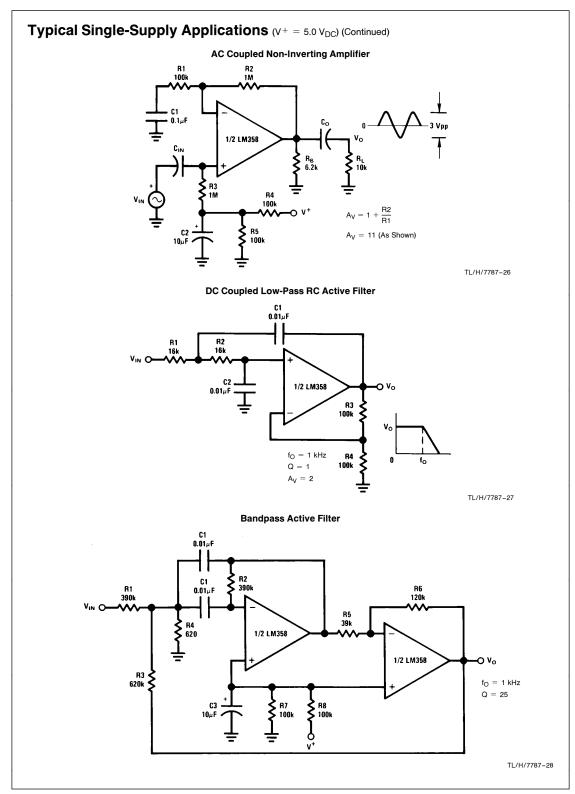


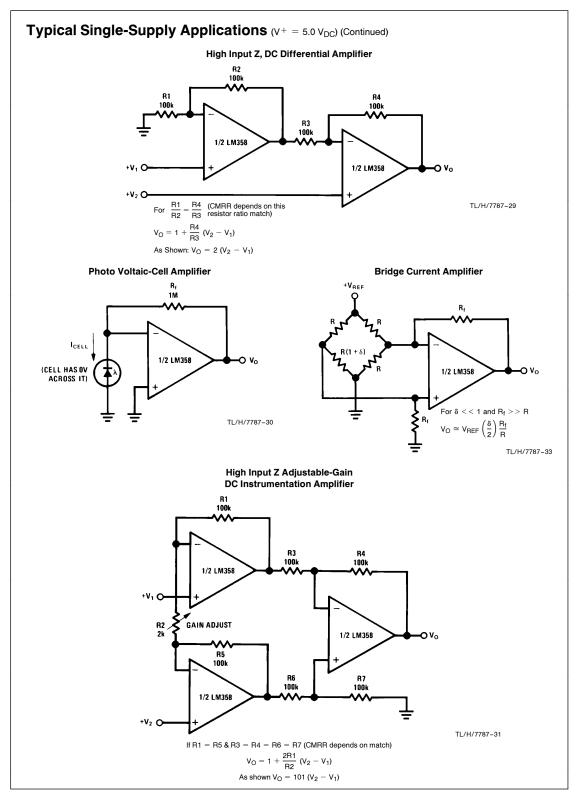




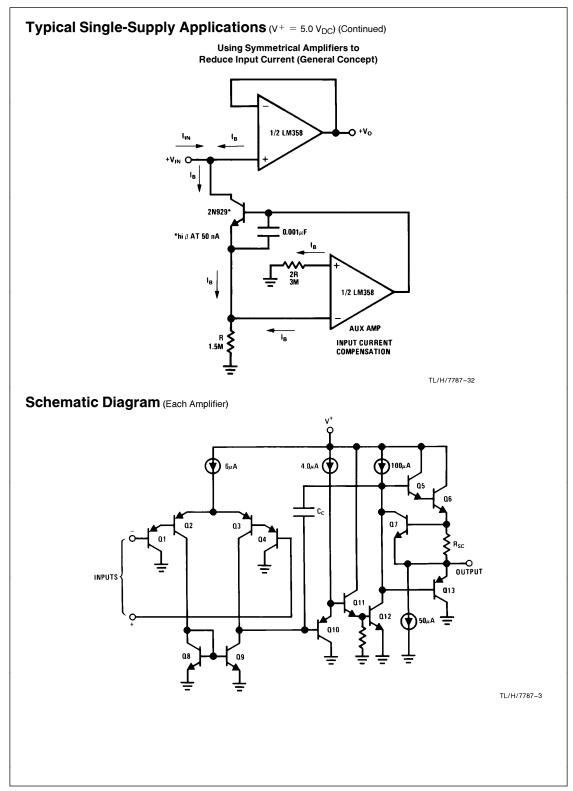


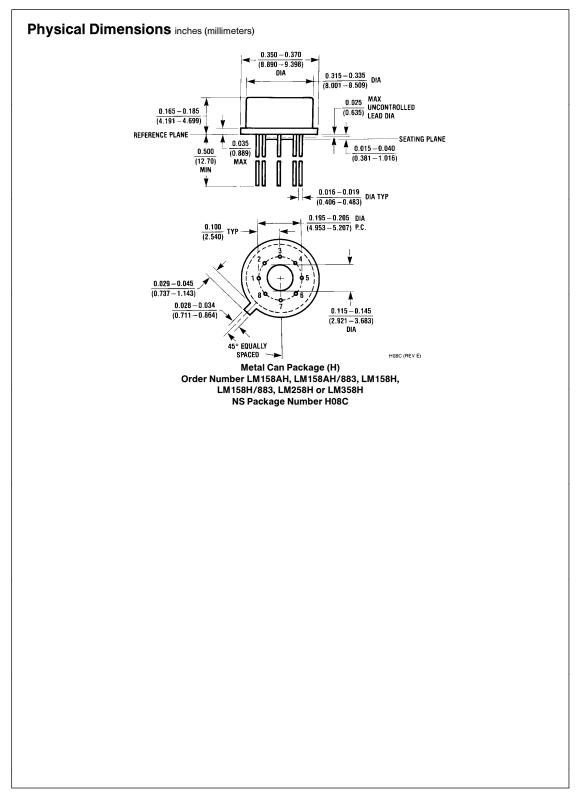


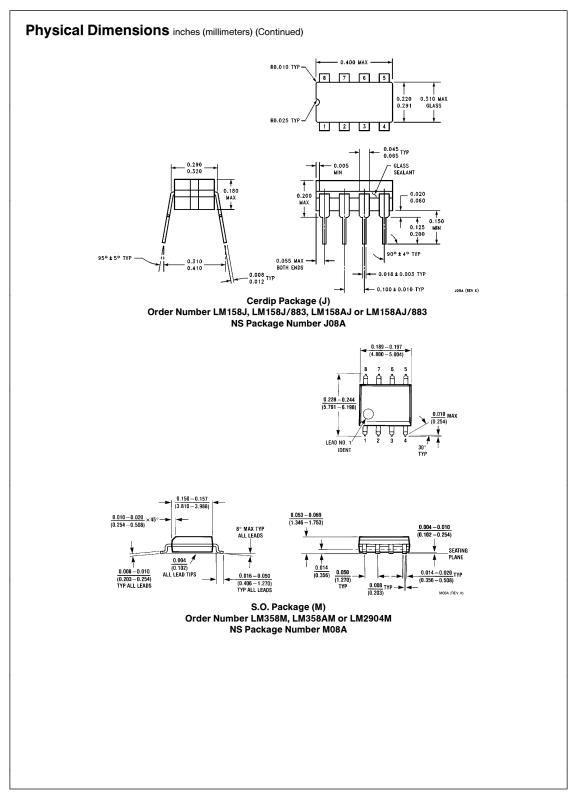


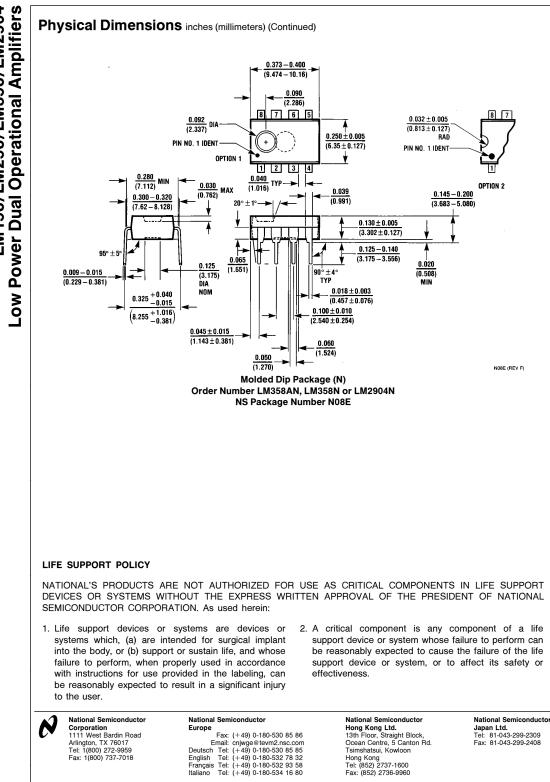












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# LM158/LM258/LM358/LM2904